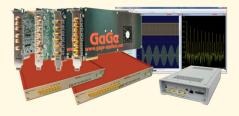


GaGe is a worldwide industry leader in high speed data acquisition solutions featuring a portfolio of the highest performance digitizers, PC oscilloscope software, powerful SDKs for custom application development, and turnkey integrated PC-based measurement systems.



APPLICATIONS

- RADAR Design and Test
- Signals Intelligence (SIGINT)
- Ultrasonic Non-Destructive Testing
- LIDAR Systems
- Communications
- Spectroscopy
- **High-Performance Imaging**
- Time of Flight
- Life Sciences
- **Particle Physics**

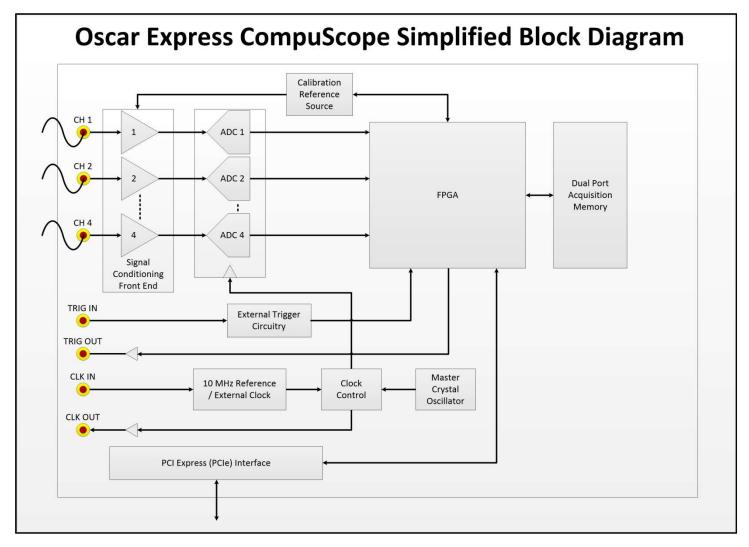
Oscar Express CompuScope 2-4 CH, 50 to 100 MS/s, 16-Bit, PCIe Digitizer



FEATURES

- 4 or 2 Digitizing Input Channels with 16-Bit Vertical A/D Resolution
- 100 MS/s or 50 MS/s Maximum Sampling Rate per Channel
- 16 Software Selectable A/D Sampling Rates from 1 kS/s to 100 MS/s
- 65 MHz Analog Input Bandwidth
- 2 GS (4 GB) Onboard Memory Standard, Expandable up to 8 GS (16 GB)
- Dual Port Memory with Sustained PCIe Data Streaming at 800 MB/s
- Full-Featured Front-End with AC/DC Coupling and 50 Ω /1M Ω Inputs
- Software Control of Input Voltage Ranges, Coupling and Impedances
- Ease of Integration with External or Reference Clock In & Clock Out
- External Trigger In & Trigger Out with Advanced Triggering Operations
- Synchronized Multi-Card Systems up to 8 Cards for 32 Channels
- Full-Height Full-Length PCI Express (PCIe) Generation 2.0 x8 Card
- Programming-Free Operation with GaGeScope PC Oscilloscope Software
- Software Development Kits Available for C/C#, LabVIEW and MATLAB
- Windows 10/8/7 and Linux Operating Systems Supported

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MAIN SPECIFICATIONS

Model #	:	<u>CSE4424</u>	<u>CSE4427</u>	<u>CSE4444</u>	<u>CSE4447</u>
# of Input Channels	:	2	2	4	4
Max. Rate per Channel	:	50 MS/s	100 MS/s	50 MS/s	100 MS/s
Vertical A/D Resolution	:	16-bit	16-bit	16-bit	16-bit

DYNAMIC PARAMETER PERFORMANCE

ENOB	:	12.0 Bits
SNR	:	75.2 dB
THD	:	-82.1 dB
SINAD	:	74.4 dB
SFDR	:	86.0 dB

Dynamic parameter measurements are done by acquiring a high purity 10 MHz sine wave with amplitude of 95% of the input range sampling at maximum 100 MS/s. These measurements were taken on the ±500 mV input range using 50 Ω termination and DC coupling and with applied anti-aliasing filter. Dynamic parameter calculations are done from a 16 kiloSample Fourier Spectrum after applying a 7-term Blackman Harris Windowing Function to the time-domain waveform.

A/D SAMPLING

ACQUISITION MEMORY

Rates per Channel, : Model dependent (software selectable)	100 MS/s, 50 MS/s, 25 MS/s, 10 MS/s, 5 MS/s, 2 MS/s, 1 MS/s, 500 kS/s, 200 kS/s, 100 kS/s,	Acquisition memory size is shared and equally divided among all active input channels (1, 2 or 4).			
(software selectable)		50 kS/s, 20 kS/s, 10 kS/s, 5 kS/s,	Standard Size	:	2 GS (4 GB)
		2 kS/s, 1 kS/s	Optional Sizes	:	4 GS (8 GB), 8 GS (16 GB)
Rate Accuracy	:	±1 part-per-million	Architecture	:	Dual Port
		(0° to 50° C ambient)	Data Streaming	:	Yes

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ANALOG INDUT CHANNELS

ANALOG INPUT CH	IAN	NELS	CLOCK IN		
Connectors	:	SMA	Connector	:	SMA
Impedance	:	50 Ω or 1M Ω (software selectable)	Signal Level	:	Minimum 1 V RMS,
Coupling	:	AC or DC (software selectable)			Maximum 2 V RMS
Analog Bandwidth	:	DC (50 Ω) = DC to 65 MHz	Impedance	:	50 Ω
		AC (1M Ω) = 10 Hz to 65 MHz	Coupling	:	AC
Voltage Ranges	:	±100 mV, ±200 mV, ±500 mV, ±1 V, ±2 V,	Duty Cycle	:	50% ±5%
		\pm 5 V, \pm 10 V, \pm 20 V, \pm 50 V (software selectable; \pm 10 V, \pm 20 V, \pm 50 V only available on 1M Ω)	Input Modes	:	External Clock or 10 MHz Reference Clock
Flatness	:	Within ±0.5 dB of ideal response to 50 MHz.	External Clock Mode Rates	:	Minimum 10 MHz to Maximum Sampling Rates of 100 MHz or 50 MHz.
		Measured at 100 MS/s in the \pm 500 mV range with 50 Ω input impedance and 95% of full scale amplitude.	External Reference Clock Mode Rate	:	10 MHz ±1000 ppm; the external reference time base is used to synchronize the internal sampling clock.
DC Accuracy	:	±0.5%. Measured on ±500 mV, ±1 V, ±2 V input ranges for both 50 Ω and 1M Ω input	CLOCK OUT		
		impedance settings.	Connector	:	SMA
DC User Offset	:	$\pm 1 \times Full Range$	Signal Level	:	0 – 1.8 V
Absolute Max.		(above ± 5 V is limited to ± 2.5 V) ± 15 V (50 O) ± 75 V (1M O on all but two	Impedance	:	50 Ω Compatible
Input	·	±15 V (50 Ω), ±75 V (1M Ω on all but two lowest Input Ranges, where Max is ±25 V)	Duty Cycle	:	50% ±10%
LOW-PASS FILTER			Output Modes	:	Maximum Sampling Clock Frequency or 10 MHz Reference Clock
Type Cut-Off Frequency	:	3-pole, 1 per Channel 25 MHz	Max. Frequency	:	Maximum Sampling Rates, 100 MHz or 50 MHz.
Operation	:	Individually Software Selectable	Min. Frequency	:	10 MHz from External Clock, 1 kHz from Internal Clock
TRIGGERING			MULTIPLE RECORD		
Engines	:	2 per Channel, 1 for External Trigger	Pre-Trigger Data	:	Up to 32 kS Total
Course		1 for External Trigger	Record Length		32 points minimum. Can be defined with
Source	•	Any Input Channel, External Trigger or Software		•	32 point resolution.
Input Combination	:	All Combinations of Sources Logically OR'ed	TIME-STAMPING		
Slope	:	Positive or Negative (software selectable)	Timing Resolution	:	One Sample Clock Cycle
Sensitivity	:	±2% of Full Scale Input Range of Trigger Source. This implies that signal amplitude	Counter Turnover	:	>48 Hours Continuous
		must be at least 4% of full scale to cause a	MULTI-CARD SYSTEMS		
		trigger to occur. Smaller signals are rejected as noise.	Master/Slave Mode	:	Provides synchronized triggering and sampling on all channels for all cards to create larger multi-channel systems.
Accuracy	:	Less than ±2% of Full Scale for Channel	Independent Mode		Each card operates independently within
Post-Trigger Data	:	Triggering 32 points minimum. Can be defined with 32		•	the system.
		point resolution.	Number of Cards	:	2 to 8 Cards for up to 32 Channels Total
EXTERNAL TRIGGE	R		DIMENSIONS		
Connector	:	SMA	Size	:	Single Slot, Full Height, Full Length
Impedance	:	2k Ω	POWER CONSUMPTION	Ν	
Coupling	:	AC or DC	Power	:	25 Watts (typical)
Bandwidth	:	>100 MHz	PC SYSTEM REQUIREM	EN	ITS
Voltage Range	:	±1 V, ±5 V (software selectable)	PCI Express (PCIe) Slot		1 Free Full-Height Full-Length PCIe Gen1, Gen2 or Gen3, x8 or x16 Slot
TRIGGER OUT Connector		SMA	Operating System	:	Windows 10/8/7 (32-bit/64-bit),
Impedance	:	50 Ω	Operating System	·	Linux – Requires SDK for C/C#
·	:	0 – 1.8 V			
Amplitude	•	U - 1.0 V			

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ORDERING INFORMATION

Model Number	A/D Resolution	# of Channels	Max. Sampling Rate per Channel	Memory Size	Order Part Number	
CSE4424	16-bit	2	50 MS/s	2 GS (4 GB)	OSC-442-004	
CSE4427	16-bit	2	100 MS/s	2 GS (4 GB)	OSC-442-007	
CSE4444	16-bit	4	50 MS/s	2 GS (4 GB)	OSC-444-004	
CSE4447	16-bit	4	100 MS/s	2 GS (4 GB)	OSC-444-007	
Memory U	pgrades					
	pgrade: 2 GS	(4 GB) to 4 G	GS (8 GB)		MEM-181-203	
Memory U	pgrade: 2 GS	(4 GB) to 8 G	GS (16 GB)		MEM-181-205	
Cable Accessories						
	SMA to BNC				ACC-001-031	
Set 4 Cable SMA to BNC					ACC-001-033	
Master/Sla	ve Upgrade	s				
Master Multi-Card Upgrade					OSC-181-012	
Slave Multi-Card Upgrade					OSC-181-013	
eXpert FPG	iA Firmware	Options				
eXpert PCIe Data Streaming					STR-181-000	
eXpert Signal Averaging					250-181-001	
GaGeScope	e Software					
GaGeScope: Lite Edition					Included	
GaGeScope: Standard Edition					300-100-351	
GaGeScope: Professional Edition					300-100-354	
Software D	evelopmen	t Kits (SDK	s)			
GaGe SDK Pack (includes C/C#, MATLAB, LabVIEW SDKs)				200-113-000		
CompuScope SDK for C/C#				200-200-101		
CompuScope SDK for MATLAB				200-200-102		
CompuScope SDK for LabVIEW						

WARRANTY

Standard two years parts and labor.

Unless otherwise specified, all dynamic performance specs have been qualified on engineering boards. All specifications subject to change without notice.

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